

Lingxiang Xiang

CONTACT INFORMATION	Hardware/Software Codesign Lab College of Computer Science Zhejiang University, Hangzhou, China	phone: 86-571-87934818 website: www.greenshell.info e-mail: lxxiang@zju.edu.cn
RESEARCH INTERESTS	My research interests lie in the area of computer architecture with a focus on high performance microarchitecture. I am particularly interested in new cache designs based on program behavior and the interaction between system software (OSes, compilers) and architecture. Going forward, I wish to engage in exciting research for enhancing hardware/software cooperation.	
EDUCATION	Zhejiang University , Hangzhou, China <i>M.S., Computer Science</i> (GPA: 89.4/100) Sep 2007 - Mar 2010 (expected) <ul style="list-style-type: none">• Advisor: Prof. Tianzhou Chen <i>B.S., Computer Science</i> (GPA: 86.2/100) Sep 2003 - Jun 2007	
HONOURS AND AWARDS	First-class Award of Honor for Graduate, <i>Zhejiang University</i> , Oct 2008. Tencent Technology Scholarship, Highest Band (2 in all CS grad. students), <i>Tencent Inc.</i> , Oct 2008. Outstanding Graduate, <i>Zhejiang University</i> , Jun 2007. Excellent Academic Scholarship, <i>Zhejiang University</i> , 2004 - 2006.	
PUBLICATIONS	<u>Lingxiang Xiang</u> , Tianzhou Chen, Qingsong Shi and Wei Hu, “Less Reused Filter: Improving L2 Cache Performance via Filtering Less Reused Lines”, In <i>the 23rd International Conference on Supercomputing (ICS)</i> . New York, USA. Jun 2009. <u>Lingxiang Xiang</u> , Jiangwei Huang and Tianzhou Chen, “Coordinating System Software for Power Savings”, In <i>the 2nd International Conference on Future Generation Communication and Networking</i> . Sanya, China. Dec 2008. Chen Tianzhou, Huang Jiangwei, Xiang Lingxiang and Shi Qingsong, “Dynamic Power Management Framework for Multi-core Portable Embedded System”, In <i>the 1st ACM/IEEE International Forum on Next-generation Multicore/Manycore Technologies</i> . Cairo, Egypt. Nov 2008. Chen Tianzhou, Huang Jiangwei, Xiang Lingxiang and Wu Xinliang, “Balance the Battery Life and Real-time Issues for Portable Real-time Embedded System by Applying DVS with Battery Model”, In <i>the 34th Annual Conf. of the IEEE Industrial Electronics Society</i> . Florida, USA. Nov 2008. <u>Xiang Lingxiang</u> , Huang Jiangwei, Sheng Weihua and Chen Tianzhou, “The Design and Implementation of the DVS Based Dynamic Compiler for Power Reduction”, In <i>the 7th International Symposium on Advanced Parallel Processing Technologies</i> . Guangzhou, China. Nov 2007.	
ACADEMIC EXPERIENCE	Hardware/Software Codesign Lab, Zhejiang University Jul 2007 - present <i>Graduate Student Research Assistant</i> Hardware/Software Codesign Lab, Zhejiang University Feb 2007 - Jun 2007 Participated a NSF research program focused on power-aware computing. Designed a DVS-based dynamic compiler for power-savings and a physical platform that can measure and record a CPU’s real power consumption. Advanced Computing and System Lab, Zhejiang University Jul 2006 - Dec 2006 Designed and implemented general algorithms for SONAR, a speech and speaker recognition system funded by the nation.	
CONFERENCE TALKS	“Less Reused Filter: Improving L2 Cache Performance via Filtering Less Reused Lines”, <i>23rd International Conference on Supercomputing (ICS)</i> . New York, USA. Jun 2009. “The Design and Implementation of the DVS Based Dynamic Compiler for Power Reduction”, <i>7th International Symposium on Advanced Parallel Processing Technologies</i> . Guangzhou, China. Nov 2007.	
TECHNICAL SKILLS	Familiar with hacking Linux kernel. Familiar with various architectural simulators and tools including Simics, M5, SESC, PTLSim, Pin, etc. <i>Program Language</i> : C, C++, Assembly language, Python, Linux shell scripting, Verilog, VB.	